

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SEMICONDUCTOR EMBEDDED MEMORY DEVICES HAVING BIST CIRCUIT SITUATED UNDER THE BONDING PADS				
Application Number : Date : First Named Applicant: Ming-Jing HO Confirmation Number: Attorney Docket Number: NAUP0567USA					
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td></tr><tr><td>Winston Hsu Registered Number: 41,526</td><td>Winston Hsu</td></tr></table>		Submitted By:	Elec. Sign.	Winston Hsu Registered Number: 41,526	Winston Hsu
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Winston Hsu Registered Number: 41,526	Winston Hsu				

Documents being submitted:	Files
us-power-of-attorney-grant	NAUP0567-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	NAUP0567-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0567ASS1.tif
us-request	NAUP0567-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	NAUP0567-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	NAUP0567-usdecl.xml us-declaration.dtd us-declaration.xsl
application-body	NAUP0567-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Naup0567usa-01.tif NAUP0567DEC1.tif NAUP0567DEC2.tif
us-declaration	
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Comments	